

PATENT NUMBER

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U.S. **UTILITY** Patent Application

O.I.P.E. <i>MR</i> SCANNED <i>AC 12</i> Q.A. <i>MS</i>	PATENT DATE
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APPLICATION NO. 09/930607	CONT/PRIOR	CLASS <del>326</del> 417	SUBCLASS 9	ART UNIT <del>5123</del>	EXAMINER David Mark D
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Reto Stamm  
 Ciaran McGloin  
 David McNicholl

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BA-NH TAT

TITLE	APPLICANTS
1. <b>Chairman</b>	1. <b>Mr. J. Edgar Hoover</b>
2. <b>Vice Chairman</b>	2. <b>Mr. J. Edgar Hoover</b>
3. <b>Members</b>	3. <b>Mr. J. Edgar Hoover</b>
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Instruction processor and programmable logic device cooperative computing arrangement and method

PTO-2040  
12/89

## ISSUING CLASSIFICATION

[illegible]

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<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner)		<b>NOTICE OF ALLOWANCE MAILED</b>	
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<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Primary Examiner)		Amount Due	Date Paid
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